

**ABSTRACT**

A piezo actuator drive circuit (100) adapted to be calibrated in a voltage mode. The present invention achieves technical advantages as a piezo actuator driver that can be calibrated in the voltage mode by switching a drive amplifier (42) output to a high impedance state and driving a load  $C_{\text{piezo}}$  with a fixed current source ( $I_{\text{ref}}$ ) to characterize the load. Preferably, feedback resistors ( $R_1$ ,  $R_2$ ) configured as a resistive divide network are used as a sensor feeding and analog to digital converter.

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